



# 25th ACM International Systems and Software Product Line Conference (SPLC 2021)

## **WEESR and REVE 2021**

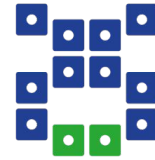
**4th Workshop on Experiences and Empirical Studies on Software Reuse**  
**9th International Workshop on Reverse Variability Engineering**

*Opening and welcome notes*

# Thanks to our Sponsors



**BOSCH**



pure  
systems



MetaCase



Association for  
Computing Machinery



**SIGSOFT**  
SPECIAL INTEREST GROUP ON SOFTWARE ENGINEERING

# WEESR & REVE Organizers

**Jaime Chavarriaga**, Universidad de Los Andes, Colombia



**Julio Ariel Hurtado**, Universidad del Cauca, Colombia



**Wesley K. G. Assunção**, Johannes Kepler University Linz, Austria



**Roberto E. Lopez-Herrejon**, Ecole de technologie supérieure, Montreal, Canada



**Tewfik Ziadi**, Sorbonne University & RedFabriQ, Paris, France



**Jabier Martinez**, Tecnalía, Spain



# History of WEESR

Workshop on Experiences and Empirical Studies on Software Reuse  
(4th edition today)

*An space where **researchers and practitioners** can present their **experiences and studies on the area of software reuse**, discuss the **challenges that must be overcome** in non-academic environments and obtain feedback on **how the corresponding empirical research may be conducted and improved***

- WEESR@ICAI 2018
- WEESR@SPLC 2019 and 2020

# History of REVE

## Workshop on Reverse Engineering for Variability (9th edition today)

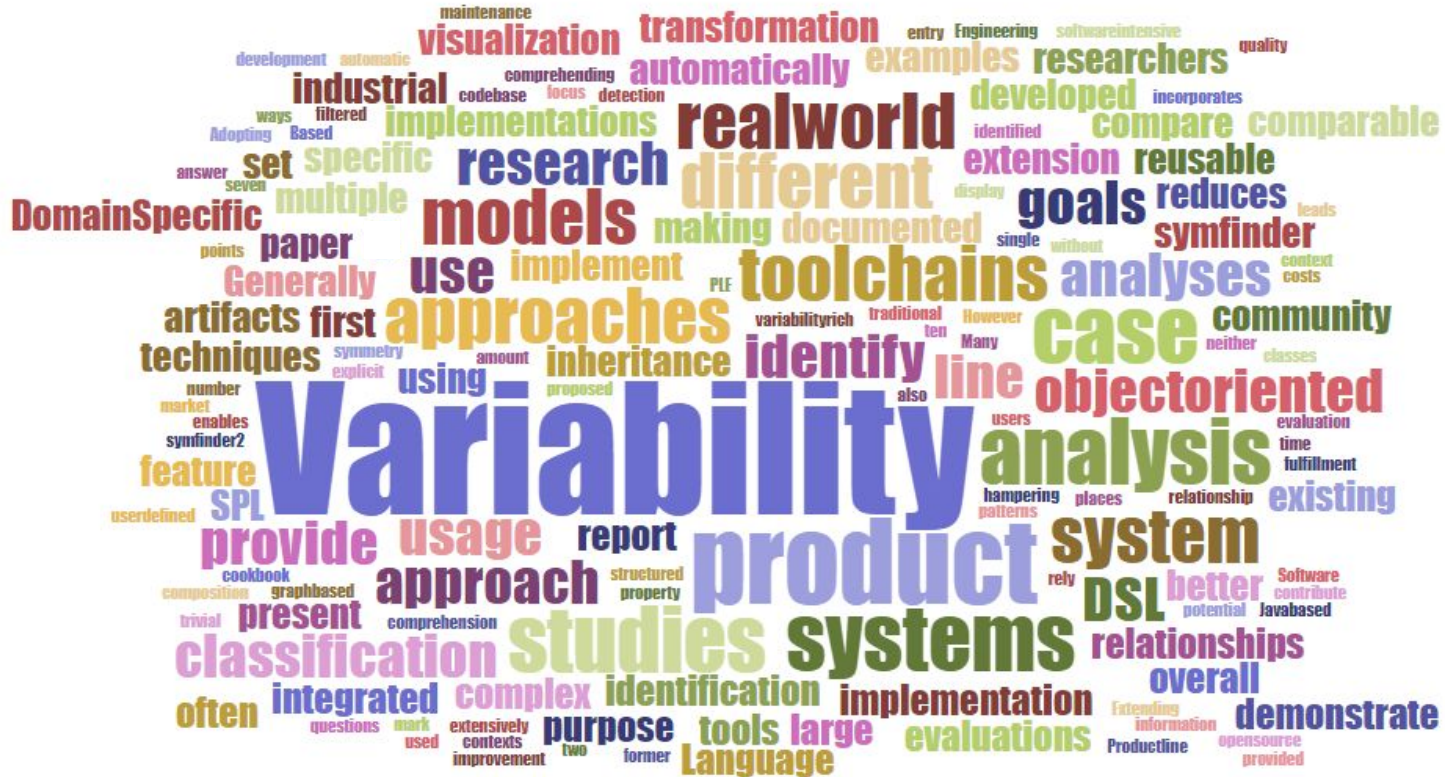
*This workshop aims to foster research about making the most of the two main inputs for **SPL migration**:*

*1) **domain knowledge** and 2) **legacy assets***







***Processes, techniques, tools, or empirical studies** related to the automatic, semi-automatic or manual **extraction or refinement of SPL assets***

- **REVE@SPLC** from 2014 to 2021
- **REVE@CSMR** 2013

# Titles and abstracts



# REVE/WEESR Workshop Schedule

12:00 - 12:15	Opening and welcome notes by workshop organizers
12:15 - 13:00	Keynote: Paul Grünbacher <b><i>Risks and Opportunities of the Research Loop in Variability Engineering</i></b> 
13:00 - 13:20	Paper presentation: <i>Johann Mortara, Xhevahire Tërnavaj, Philippe Collet and Anne-Marie Déry</i> . <b>Extending the Identification of Object-Oriented Variability Implementations using Usage Relationships</b> 
13:20 - 13:40	Paper presentation: <i>Vasil Tenev, David Morais Ferreira and Martin Becker</i> . <b>Product-Line Analysis Cookbook: A Classification System for Complex Analysis Toolchains</b> 
13:40 - 13:55	<b>15 minutes Break</b> 
13:55 - 14:15	Paper presentation: <i>Kristof Meixner, Kevin Feichtinger, Rick Rabiser and Stefan Biff</i> . <b>A Reusable Set of Real-World Product Line Case Studies for Comparing Variability Models in Research and Practice</b> 
14:15 - 14:35	Journal-First talk: Robert Lindohf, Saab AB, Stockholm, Sweden <b><i>Understanding product line maturity in an organization</i></b> 
14:35 - 14:55	Workshop Discussion: all attendees, moderated by workshop organizers
14:55 - 15:00	Closing

# Thanks to the PC

## REVE

- **Barbara Gallina**, Mälardalen University, Sweden
- **Djamel Eddine Khelladi**, DIVERSE Team, IRISA-INRIA, CNRS, Université Rennes 1, France
- **Eduardo Figueiredo**, Federal University of Minas Gerais (UFMG), Brazil
- **Jaime Font**, University San Jorge, Spain
- **Jennifer Perez**, Universidad Politécnica de Madrid, Spain
- **José Galindo**, University of Sevilla, Spain
- **Julio Ariel Hurtado Garcia**, Universida del Cauca, Colombia
- **Marianne Huchard**, LIRMM, Université de Montpellier and CNRS, France
- **Mathieu Acher**, Univ Rennes, Inria, CNRS, IRISA, France
- **Oscar Diaz**, University of the Basque Country, Spain
- **Serge Demeyer**, University of Antwerp, Belgium
- **Sten Grüner**, ABB Corporate Research, Germany

## WEESR

- **Rick Rabiser**, Johannes Kepler University, Austria
- **David Benavides**, Universidad de Sevilla, Spain
- **Thomas Fogdal**, Danfoss Power Electronics A/S, Denmark
- **José Galindo**, Universidad de Sevilla, Spain
- **Jabier Martinez**, Tecnalia, Spain
- **Martin Becker**, Fraunhofer Institute for Experimental Software Engineering (IESE), Germany
- **Daniel Perovich**, Universidad de Chile, Chile
- **Carlos Parra**, Pontificia Universidad Javeriana, Colombia
- **Héctor Flórez**, Universidad Distrital Francisco José de Caldas, Colombia
- **Germán Urrego**, Universidad de Antioquia, Colombia
- **Helga Duarte**, Universidad Nacional de Colombia, Colombia
- **Maria Cecilia Bastarrica**, Universidad de Chile, Chile

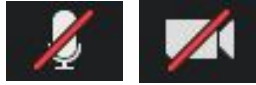




# Online workshop

- If you are not the current keynote or paper presenter

- Please



- Questions and Answers (Q&A)

- Moderated by the organizers

- Use the virtual “Raise your hand” mechanism to use video/voice for your question
    - Or use the chat to write your question



# Keynote: Paul Grünbacher



## Risks and Opportunities of the Research Loop in Variability Engineering



**Paul Grünbacher**, Associate Professor at the Institute for Software Systems Engineering at Johannes Kepler Universität Linz (Austria)

A typical research approach in software engineering is to lift engineering challenges into abstract problems, solving them, and applying the results. The keynote talk will explore this research loop in the context of (reverse) variability engineering. Based on ideas of John Regehr's interesting blog article (<https://blog.regehr.org/archives/1582>) the talk will discuss research risks and research opportunities: in particular, moving from concrete to abstract can be risky in variability engineering if the abstractions are no longer relevant for the real-world challenges to be addressed. Moving from abstract to concrete on the other hand often means solving real engineering problems but can also provide new insights leading to research opportunities. The talk will report lessons learned from working with different industry partners in the domain of variability engineering. It will cover areas such as variability modelling, variability and evolution, variability-aware analysis, as well as software process issues.